

ABSTRACT OF THE DISCLOSURE

A display panel includes first to third scan lines, first to third color component signal lines, first to third switching elements which are connected to the first to third scan lines and the first to third color component signal lines respectively and controlled
5 by first to third select signals, first to third pixel electrodes, and first to third demultiplex switching elements which output multiplexed color component signals to the color component signal lines. A select signal generation circuit generates a j th select signal ($1 \leq j \leq 3$, j is an integer) so that at least a j th switching element is in an ON state when
10 a j th demultiplex switching element shifts from an ON state to an OFF state and the j th switching element is set to an OFF state before the j th demultiplex switching element is set to the ON state again after the j th demultiplex switching element has shifted to the OFF state.